Sharad Institute of Technology College of Engineering, Yadrav (Ichalkaranji)-416121

(Affiliated to Dr. Babasaheb Ambedkar Technological University, Lonere, Approved by AICTE, New Delhi, Recognized by DTE, Government of Maharashtra)

Program of Winter Semester Examination 2025 [Old Syllabus Backlog Courses] (Third Year B. Tech Semester V)						
DAY & DATE	TIME	PROGRAM	COURSE CODE	COURSE NAME		
Thursday, 06-11-2025	10.00 AM to 12.00 PM	Computer Science and Engineering	CS501	Design Analysis & Algorithm		
		Artificial Intelligence and Data Science	AD503	Design & Analysis of Algorithm		
		Electrical Engineering	EE501	Power Electronics		
		Automation and Robotics	AR501	Industrial Automation		
		Mechanical Engineering	ME501	Heat Transfer		
		Electronics and Computer Engineering	EC501	Digital Signal Processing		
		Electronics and Telecommunication Engineering	ET501	Electromagnetic field Theory		
		Civil Engineering	CE501	Environmental Engineering		
	10.00 AM to 12.00 PM	Mechatronics Engineering	MT501	Kinematics and Theory of Machines		
Friday, 07-11-2025		Computer Science and Engineering	CS502	Advanced Java Programming		
	10.00 AM to 12.00 PM	Artificial Intelligence and Data Science	AD504	Advanced JAVA Programming		
		Electrical Engineering	EE502	AC Machines		
		Automation and Robotics	AR502	Robot Kinematics and Dynamics		
		Mechanical Engineering	ME502	Machine Design-I		
07-11-2023		Electronics and Computer Engineering	EC502	Microprocessor & Microcontroller		
		Electronics and Telecommunication Engineering	ET502	VLSI Design		
		Civil Engineering	CE502	Design of Steel Structure		
		Mechatronics Engineering	MT502	Signals and Systems		
Saturday, 08-11-2025	10.00 AM to 12.00 PM	Computer Science and Engineering	CS504	Software Engineering		
		Artificial Intelligence and Data Science	AD501	Software Engineering		
		Electrical Engineering	EE503	Microprocessor & Microcontroller		
		Automation and Robotics	AR503	Internet of Things		
		Mechanical Engineering	ME503	Metrology and Quality Control		
		Electronics and Computer Engineering	EC503	Java Programming		
		Electronics and Telecommunication Engineering	ET504	Power Electronics		
		Civil Engineering	CE503	Structural Analysis-II		
		Mechatronics Engineering	MT504	Embedded System		

Shri.Shamrao Patil (Yadravkar) Educational & Charitable Trust's

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(An Autonomous Institute)

Program of Winter Semester Examination 2025	Old Syllabus Backlog Courses] (Third Year B. Tech Semester	$\overline{\mathbf{V}}$
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DAY & DATE	TIME	PROGRAM	COURSE CODE	COURSE NAME
Monday, 10-11-2025	10.00 AM to 12.00 PM	Computer Science and Engineering	CS503	Elective-II
		Artificial Intelligence and Data Science	AD502	Data Science and Visualization
		Electrical Engineering	EE504	Elective-II
		Automation and Robotics	AR504	Elective-I
		Electronics and Computer Engineering	EC504	Elective-II
		Electronics and Telecommunication Engineering	ET503	Elective-II
		Mechanical Engineering	ME504	Elective-II
		Civil Engineering	CE504	Elective-II
		Mechatronics Engineering	MT503	Elective-I
	10.00 AM to 12.00 PM	All Programms	OE511	Open Elective I (Engineering Management)
			OE512	Open Elective I (Software Testing)
			OE514	Open Elective I (Electrical Measurement)
			OE516	Open Elective I (Data Structure)
Tuesday, 11-11-2025			OE518	Open Elective I (Robotics Engineering)
			OE513	Open Elective I (Engineering Instruments and Measurement
			OE515	Open Elective I (Analog Communication Systems / Neural Network and Fuzzy Logic)
			OE517	Open Elective I (Fundamental of Mechatronics)
		Automation and Robotics	AR505	Fluid and Thermal Engineering
		Mechatronics Engineering	MT505	Industrial Automation and Robotics

Dr. Mangesh S. Kulkarni
Controller of Examinations



Dr. Bahubali B. Sangame
I/C Principal