

Sharad Institute of Technology College of Engineering, Yadrav (Ichalkaranji)-416121

13 1 MAY 2025

(Affiliated to Dr. Babasaheb Ambedkar Technological University, Lonere, Approved by AICTE, New Delhi, Recognized by DTE, Government of Maharashtra)

(An Autonomous Institute)

Program of	Winter Semester	Examination	2024 (	Third Y	ear B.	Tech	Semester '	V) (Carry	On)
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DAY & DATE TIME		PROGRAM	COURSE CODE	COURSE NAME	
Monday,	10.00 IN ( 10.00 PM (	Computer Science and Engineering	CS501	Design Analysis & Algorithm	
23-06-2025	10.00 AM to 12.00 PM	Electronics and Computer Engineering	EC501	Digital Signal Processing	
Tuesday, 24-06-2025	44 00 DV	omputer Science and Engineering CS502 Advanced Java		Advanced Java Programming	
	10.00 AM to 12.00 PM	Electronics and Computer Engineering	EC502	Microprocessor & Microcontroller	
Wednesday,		Computer Science and Engineering	CS504	Software Engineering	
25-06-2025	10.00 AM to 12.00 PM	Electronics and Computer Engineering	EC503	Java Programming	
Thursday, 26-06-2025		Computer Science and Engineering	CS503	Elective-II	
	10.00 AM to 12.00 PM	Electronics and Computer Engineering	EC504	Elective- II	
Friday, 27-06-2025	10.00 AM to 12.00 PM	All Programms	OEXXX	Open Elective-I	

Mr. Mangesh S. Kulkarni Controller of Examinations



Dr. Sanjay A. Khot Principal