Shri.Shamrao Patil (Yadravkar) Educational & Charitable Trust's

Sharad Institute of Technology College of Engineering, Yadrav (Ichalkaranji)-416121

(Affiliated to Dr. Babasaheb Ambedkar Technological University, Lonere, Approved by AICTE, New Delhi, Recognized by DTE, Government of Maharashtra)

(An Autonomous Institute)

15 SEP 2026

Program of Winter Semester Examination 2026 [Old Syllabus Backlog Courses] (Third Year B. Tech Semester V)

DAY & DATE	TIME	PROGRAM	COURSE CODE	COURSE NAME
Monday, 19/10/2026	10.00 AM to 12.00 PM	Computer Science and Engineering	CS501	Design Analysis & Algorithm
		Artificial Intelligence and Data Science	AD503	Design & Analysis of Algorithm
		Electrical Engineering	EE50l	Power Electronics
		Automation and Robotics	AR501	Industrial Automation
		Mechanical Engineering	ME50l	Heat Transfer
		Electronics and Computer Engineering	EC501	Digital Signal Processing
		Electronics and Telecommunication Engineering	ET501	Electromagnetic field Theory
		Civil Engineering	CE501	Environmental Engineering
	10.00 AM to 12.30 PM	Mechatronics Engineering	MT50l	Kinematics and Theory of Machines
Wednesday, 21/10/2026	10.00 AM to 12.00 PM	Computer Science and Engineering	CS502	Advanced Java Programming
		Artificial Intelligence and Data Science	AD504	Advanced JAVA Programming
		Electrical Engineering	EE502	AC Machines
		Automation and Robotics	AR502	Robot Kinematics and Dynamics
		Mechanical Engineering	ME502	Machine Design-I
		Electronics and Computer Engineering	EC502	Microprocessor & Microcontroller
		Electronics and Telecommunication Engineering	ET502	VLSI Design
		Civil Engineering	CE502	Design of Steel Structure
		Mechatronics Engineering	MT502	Signals and Systems
Thursday, 22/10/2026	10.00 AM to 12.00 PM	Computer Science and Engineering	CS504	Software Engineering
		Artificial Intelligence and Data Science	AD501	Software Engineering
		Electrical Engineering	EE503	Microprocessor & Microcontroller
		Automation and Robotics	AR503	Internet of Things
		Mechanical Engineering	ME503	Metrology and Quality Control
		Electronics and Computer Engineering	EC503	Java Programming
		Electronics and Telecommunication Engineering	ET504	Power Electronics
		Civil Engineering	CE503	Structural Analysis-II
		Mechatronics Engineering	MT504	Embedded System

EXAMINATION SECTION

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Program of Winter Semester Examination 2026 [Old Syllabus Backlog Courses] (Third Year B. Tech Semester V)

DAY & DATE	TIME	PROGRAM	COURSE CODE	COURSE NAME
Friday, 23/10/2026	10.00 AM to 12.00 PM	Computer Science and Engineering	CS503	Elective-II
		Artificial Intelligence and Data Science	AD502	Data Science and Visualization
		Electrical Engineering	EE504	Elective-II
		Automation and Robotics	AR504	Elective-I
		Electronics and Computer Engineering	EC504	Elective-II
		Electronics and Telecommunication Engineering	ET503	Elective-II
		Mechanical Engineering	ME504	Elective-II
		Civil Engineering	CE504	Elective-II
		Mechatronics Engineering	MT503	Elective-I
Saturday, 24/10/2026	10.00 AM to 12.00 PM	All Programms	OE511	Open Elective I (Engineering Management)
			OE512	Open Elective I (Software Testing)
			OE514	Open Elective I (Electrical Measurement)
			OE516	Open Elective I (Data Structure)
			OE518	Open Elective I (Robotics Engineering)
			OE513	Open Elective I (Engineering Instruments and Measurement)
			OE515	Open Elective I (Analog Communication Systems / Neural Network and Fuzzy Logic)
			OE517	Open Elective I (Fundamental of Mechatronics)
		Automation and Robotics	AR505	Fluid and Thermal Engineering
		Mechatronics Engineering	MT505	Industrial Automation and Robotics

Dr. Mangesh S. Kulkarni
Controller of Examinations

EXAMINATION SECTION

Dr. Bahubali B. Sangame
I/C Principal